

High-reliability discrete products and engineering services since 1977

C180A-C180PC SERIES

SILICON CONTROLLED RECTIFIERS

FEATURES

- Available as "HR" (high reliability) screened per MIL-PRF-19500, JANTX level. Add "HR" suffix to base part number.
- Available as non-RoHS (Sn/Pb plating), standard, and as RoHS by adding "-PBF" suffix.

MAXIMUM RATINGS

Characteristics	Symbol	C180	Units
RMS on-state current	I _{TRMS}	235	Α
Average on-state current	I _{T(AV)}	150	А
Peak one-cycle surge (non-repetitive) on-state current (60Hz)	I _{TSM}	3500	А
Peak one-cycle surge (non-repetitive) on-state current (50Hz)	I _{TSM}	3200	А
Critical rate of rise of on-state current (non-repetitive)	di/dt	800	A/μs
Critical rate of rise of on-state current (repetitive)	di/dt	150	A/μs
I ² t (for fusing), 8.3 ms	I²t	50,800	A ² s
Peak gate power dissipation	P _{GM}	10	W
Average gate power dissipation	P _{G(AV)}	2	W
Storage temperature	T _{stg}	-40 to +150	°C
Operating temperature	T _J	-40 to +125	°C
Mounting torque		250 to 300	In Lb
Mounting torque		28 to 34	N-m

VOLTAGE RATINGS

Characteristics	C180A	C180B	C180C	C180D	C180E	C180N	C180S	C180M	C180T	C180P	C180PB	C180PC
Working peak reverse voltage	200	300	400	500	600	700	800	900	1000	1100	1200	1300

ELECTRICAL CHARACTERISTICS (T_c = 25°C unless otherwise specified)

Characteristics	Symbol	Test Conditiions	C180	Units
Voltage – Blocking State Maximums Forward leakage, peak	I _{DRM}	T _J = 125°C, V _{DRM} = Rated	20	mA
Reverse leakage, peak	I _{RRM}	T _J = 125°C, V _{RRM} = Rated	20	mA
Current – Conducting State Maximums Peak on-state voltage	V _{TM}	T _C = 25°C, I _{TM} = 1500A peak	2.85	V
Switching Typical turn-off time	tq	I_T = 250A, T_J = 125°C, dig/dt = 12.5A/ μ sec, reapplied dv/dt = 20V/ μ sec, linear to 0.8V, V_R = 50V	100	μѕес
Typical delay time	t _d	I_T = 100Adc, V_{DRM} = Rated, gate supply = 10V open circuit, $25\Omega,~0.1\mu sec$ rise time	1.0	μѕес
Minimum critical dc/dt exponential to V _{DRM}	dv/dt	T」= 125°C, gate open	200	V/µsec
Thermal Maximum thermal resistance, junction to case	R _{ÐJC}		0.14	°C/W
Case to sink, lubricated	Recs		0.075	°C/W
Gate – Maximum Parameter Gate current to trigger	I _{GT}	$T_{C} = 25^{\circ}C$, $V_{D} = 6V$, $R_{L} = 3\Omega$	150	mA
Gate voltage to trigger	V _{GT}	$V_D = 6V$, $T_C = -40$ to $+125$ °C, $R_L = 3\Omega$	3.0	Volts
Non-triggering gate voltage	V_{GDM}	T_J = 125°C, Rated V_{DRM} , R_L = 1000 Ω	0.15	V
Peak forward gate current	I _{GTM}		10	Α



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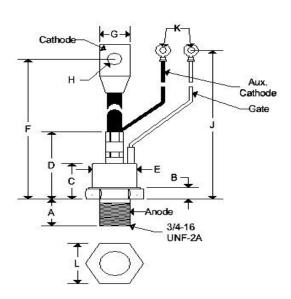
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ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise specified)

Characteristics	Symbol	Test Conditiions	C180	Units
Peak reverse gate voltage	V_{GRM}		5	V

MECHANICAL CHARACTERISTICS

Case:	TO-93			
Marking:	Alpha-numeric			
Polarity:	Cathode band			



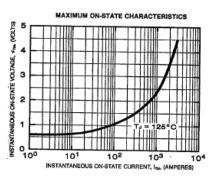
	TO-93					
	Inches	Millimeters				
	NOMINAL	NOMINAL				
Α	1.060	26.900				
В	0.550	14.000				
С	1.500	38.100				
D	2.250	57.200				
Ε	1.070	27.200				
F	7.910	200.900				
G	0.630	16.000				
Н	0.281	7.140				
J	7.910	200.900				
K	0.146	3.710				
L	1.245	31.620				

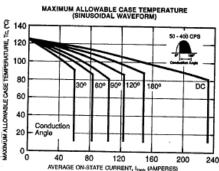


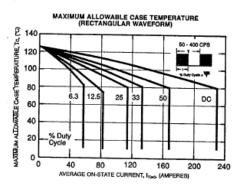
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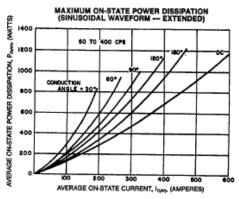
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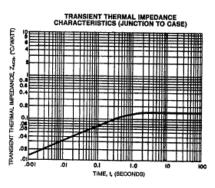
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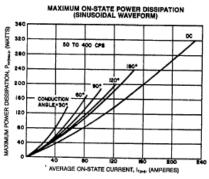


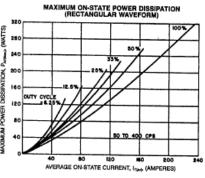


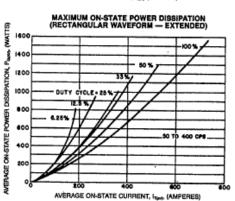










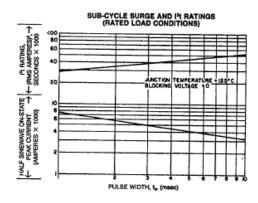


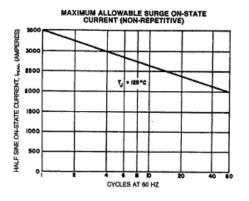


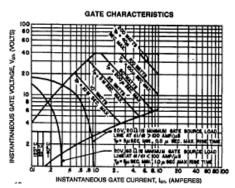
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- NOTES:

 1. Maximum altowable average gate dissipation = 5 watts,

 2. The locus of possible do trigger points in outside the boundaries shown at variemperature.

 3. Tp = Rectangular gate current pulse width (5µs min. duration; 1.0µs max. ris 20%, 650 source),

 4. 20% 20% is the minimum gate source load line when rate of circuit current risk may be a mode rate of current rise > 200 Amps/µs (\$\frac{1}{2}\$ = 5µs min., 0.5µs time).

Maximum long-term repetitive anode di/dt = 500 Amps/µs with 20V = 20Ω gate source.